Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

2SK3078A

VHF/UHF Band Amplifier Applications

(Note)The TOSHIBA products listed in this document are intended for high frequency Power Amplifier of telecommunications equipment. These TOSHIBA products are neither intended nor warranted for any other use. Do not use these TOSHIBA products listed in this document except for high frequency Power Amplifier of telecommunications equipment.

Output power: P₀ ≥ 28.0dBmW

• Gain: $G_p \ge 8.0 dB$

• Drain Efficiency: $\eta D \ge 50\%$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	10	V
Gate-source voltage	V_{GSS}	5	٧
Drain current	ΙD	0.5	Α
Power dissipation	P _D (Note 1)	3	W
Channel temperature	T _{ch}	150	°C
Storage temperature range	T _{stg}	-45~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

4.6MAX. 1.6MAX. 1.6MAX. 1.6MAX. 1.7MAX. 0.4±0.05 1.7MAX. 0.4±0.05 1.5±0.1 1.5±

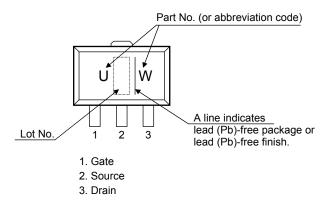
Weight: 0.05 g (typ.)

reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Tc = 25°C

Marking



Caution: This device is sensitive to electrostatic discharge.

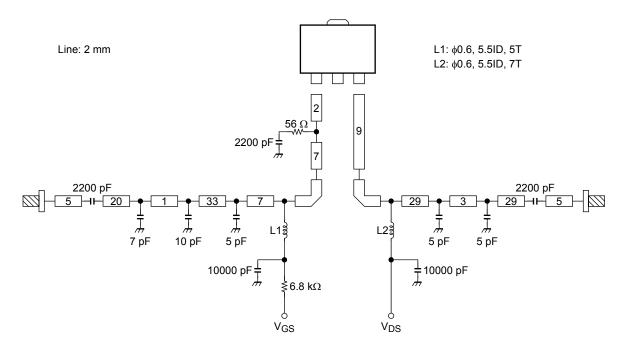
Please make enough tool and equipment earthed when you handle.

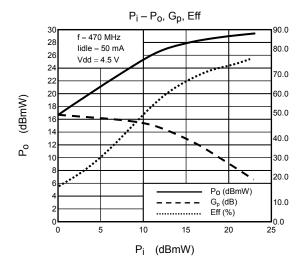
Electrical Characteristics (Ta = 25°C)

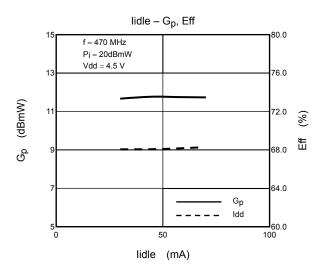
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output power	Po	V_{DS} = 4.5 V, lidle = 50 mA (V_{GS} = adjust) f = 470 MHz, P_i = 20dBmW	28.0	_	_	dBmW
Drain efficiency	ηD		50	_	_	%
Power gain	Gp	$Z_G = Z_L == 50 \Omega$	8.0	_	_	dB
Threshold voltage	V _{th}	V _{DS} = 4.8 V, I _D = 0.5 mA	0.20	_	1.20	٧
Drain cut-off current	I _{DSS}	V _{DS} = 10 V, V _{GS} = 0 V	_	_	10	μA
Gate-source leakage current	I _{GSS}	V_{GS} = 5 V, V_{DS} = 0 V	_	_	5	μA
Load mismatch (Note 2)	_	V_{DS} = 6.5 V, f = 470 MHz, P_i = 20dBmW, P_0 = 28.0dBmW (V _{GS} = adjust) VSWR LOAD 10:1 all phase	No degradation			

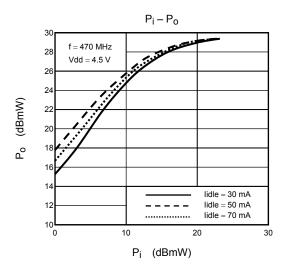
Note 2: These characteristic values are measured using measurement tools specified by Toshiba.

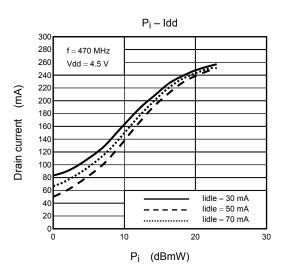
PF Output Power Test Fixture

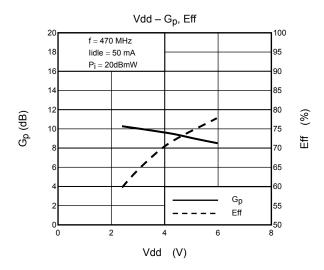


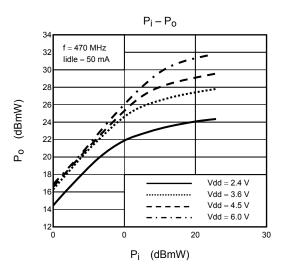




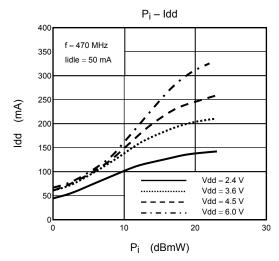








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Note3: These are typical curves and devices are not necessarily guaranteed at these curves.

RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

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- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
 In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
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